

## Dual N-Channel Enhancement Mode MOSFET

### Feature

### Pin Description

- 40V/33A  
 $R_{DS(ON)} = 17m \text{ (typ.) @VGS} = 10V$   
 $R_{DS(ON)} = 20 m \text{ (typ.) @VGS} = 4.5V$
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available  
 (RoHS Compliant)

Pin1

### Applications

- DC-DC
- Motor control

### Ordering and Marking Information

C2 HYG190ND04 XYMXXXXXX	Package Code C2: PDFN8L(5x6)  Date Code XYMXXXXXX
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Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish; which are fully compliant with RoHS. HUAYI halogen

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
<b>Common Ratings</b> (Tc=25°C Unless Otherwise Noted)				
V <sub>DSS</sub>	Drain-Source Voltage	40	V	
V <sub>GSS</sub>	Gate-Source Voltage	±20	V	
T <sub>J</sub>	Junction Temperature Range	-55 to 175	°C	
T <sub>STG</sub>	Storage Temperature Range		°C	
I <sub>S</sub>	Source Current-Continuous(Body Diode)	Tc=25°C	33	A
<b>Mounted on Large Heat Sink</b>				
I <sub>DM</sub>	Pulsed Drain Current *	Tc=25°C	99	A
I <sub>D</sub>	Continuous Drain Current	Tc=25°C	33	A
		Tc=100°C	23	A
P <sub>D</sub>	Maximum Power Dissipation	Tc=25°C	40	W
		Tc=100°C	20	W
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case		3.79	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient **		80	°C/W
E <sub>AS</sub>	Single Pulsed-Avalanche Energy ***	L=0.1mH	20	mJ

Note: \* Repetitive rating; pulse width limited by max.junction temperature.  
 \*\* Surface mounted on 1in2 FR-4 board.  
 \*\*\* Limited by T<sub>Jmax</sub>, starting T<sub>J</sub>=25°C, L = 0.1mH, R<sub>G</sub>= 25Ω, V<sub>GS</sub>=10V.

## Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190ND04LR1			Unit
			Min	Typ.	Max	
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	40	-	-	V
I <sub>DSS</sub>	Drain-to-Source Leakage Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	μA
		T <sub>J</sub> =125°C	-	-	50	μA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1	1.7	3	V
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =10A	-	17	20	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V, I <sub>DS</sub> =10A	-	20	26	mΩ
<b>Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> =10A, V <sub>GS</sub> =0V	-	0.86	1.20	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =10A, dI <sub>SD</sub> /dt=100A/μs	-	8	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	4	-	nC

## Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190ND04LR1			Unit
			Min	Typ.	Max	
<b>Dynamic Characteristics</b>						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	-	2.1	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, Frequency=1MHz	-	566	-	pF
C <sub>oss</sub>	Output Capacitance					
C <sub>rss</sub>	Reverse Transfer Capacitance					
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> =20V, R <sub>G</sub> =2.5 Ω, I <sub>DS</sub> =10A, V <sub>GS</sub> =10V	-	7	-	ns
T <sub>r</sub>	Turn-on Rise Time					
t <sub>d(OFF)</sub>	Turn-off Delay Time					
T <sub>f</sub>	Turn-off Fall Time					
<b>Gate Charge Characteristics</b>						
Q <sub>g</sub>	Total Gate Charge(V <sub>GS</sub> =10V)	V <sub>DS</sub> =32V, I <sub>DS</sub> =10A	-	14	-	nC
Q <sub>g</sub>	Total Gate Charge(V <sub>GS</sub> =4.5V)		-	7	-	
Q <sub>gs</sub>	Gate-Source Charge		-	2	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	4	-	
V <sub>plateau</sub>	Gate plateau voltage		-	3.4	-	V

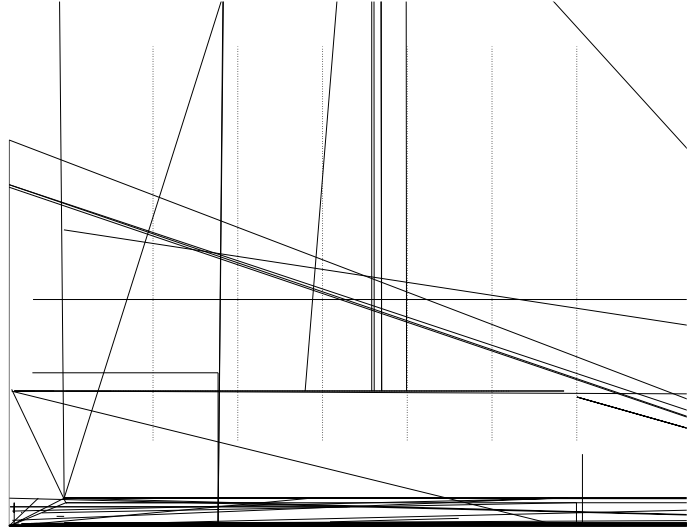
Note: \*Pulse test, pulse width 300us, duty cycle 2%

**Typical Operating Characteristics**

**Figure 1: Power Dissipation**

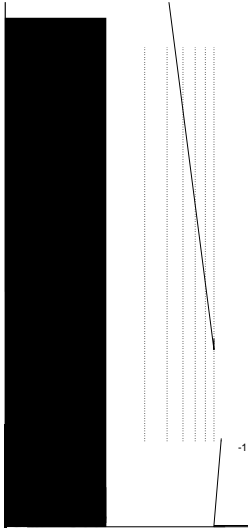


**Figure 2: Drain Current**

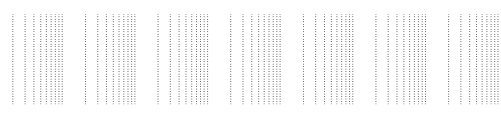


Case Tc

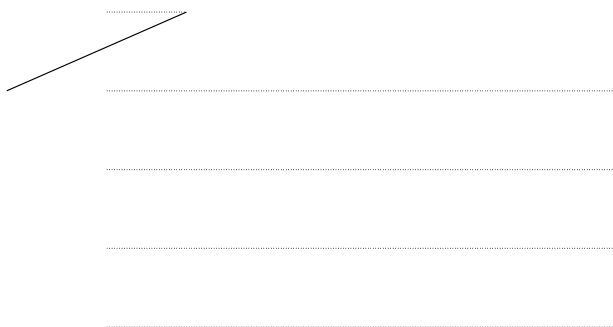
**Figure 3: Safe Operation Area**



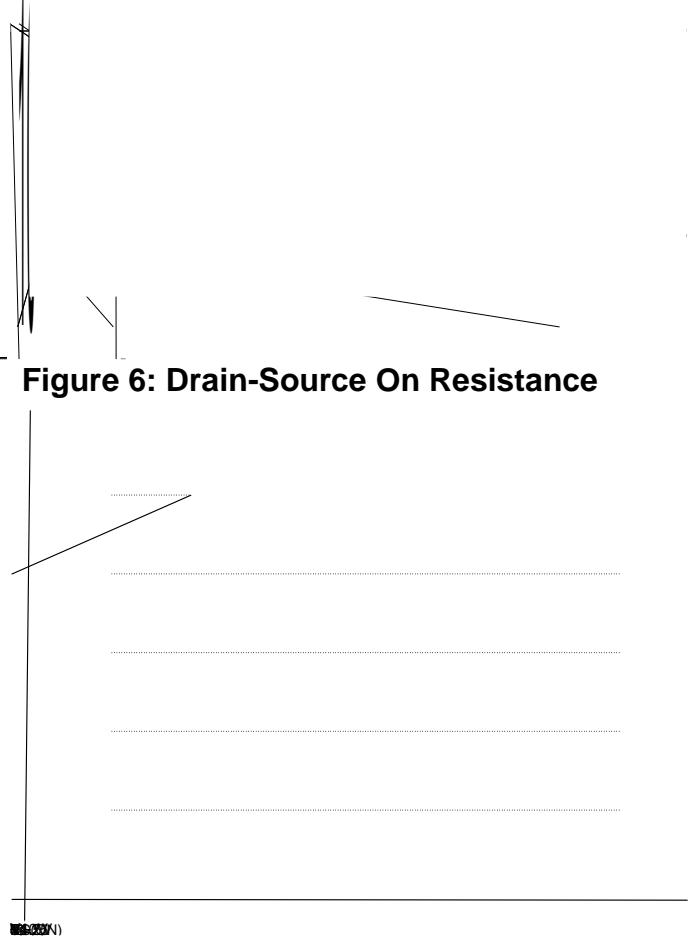
**Figure 4: Thermal Transient Impedance**



**Figure 5: Output Characteristics**



**Figure 6: Drain-Source On Resistance**



## Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

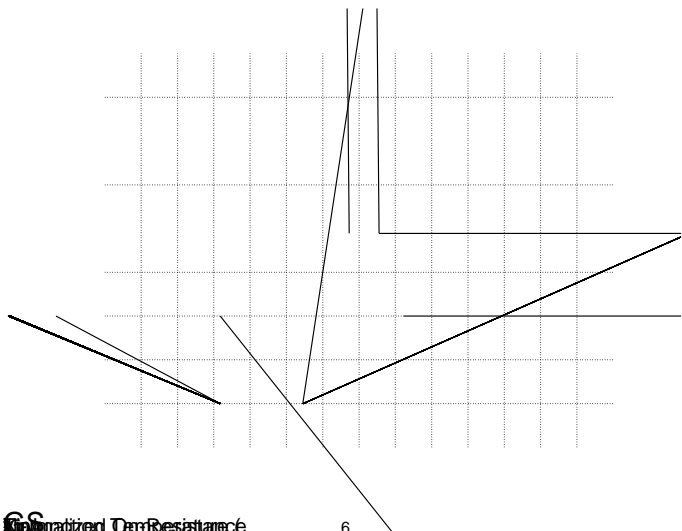


Figure 8: Source-Drain Diode Forward



Figure 9: Capacitance Characteristics

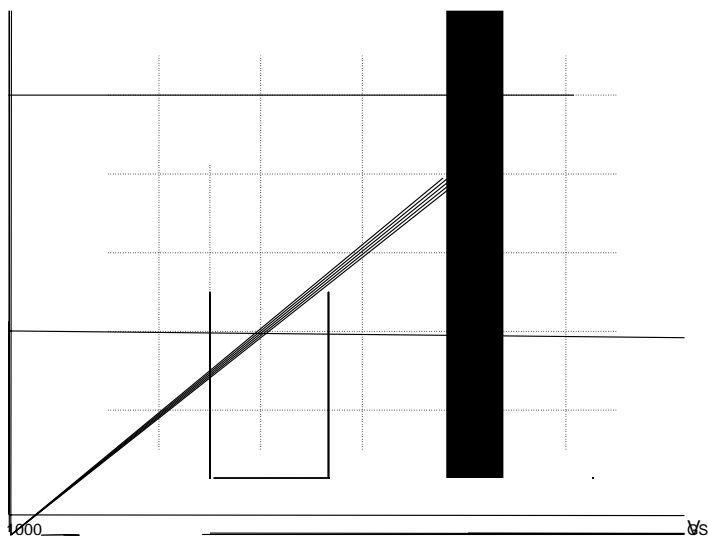
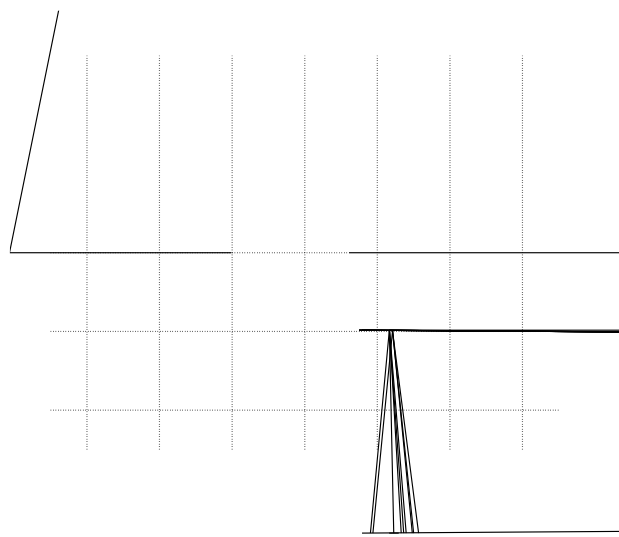
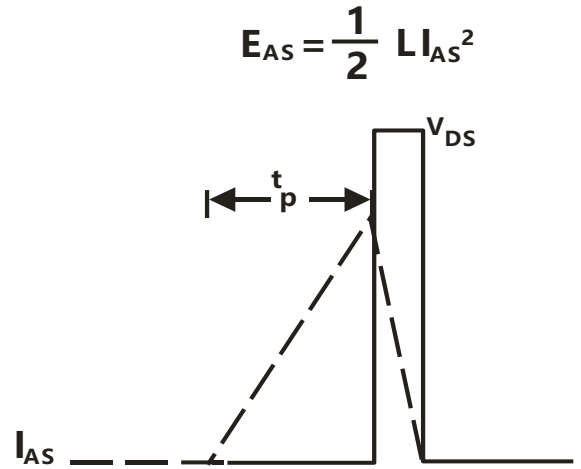
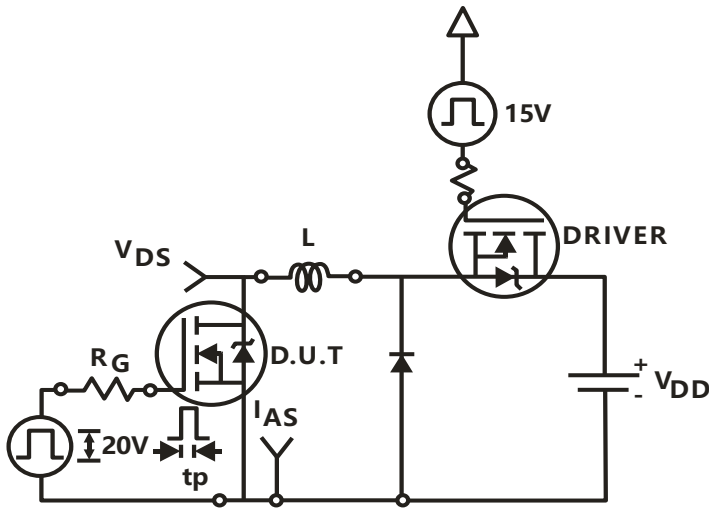


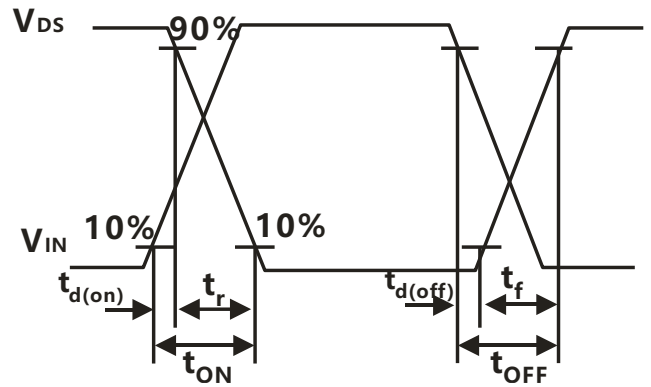
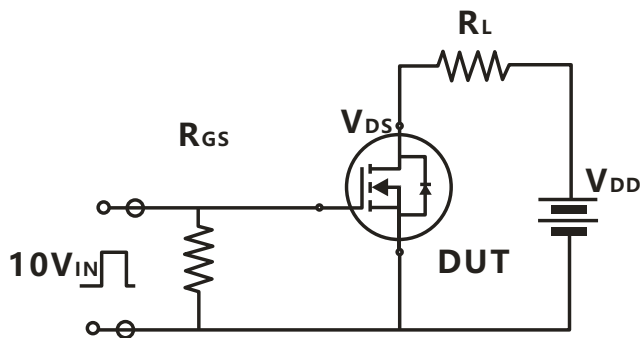
Figure 10: Gate Charge Characteristics



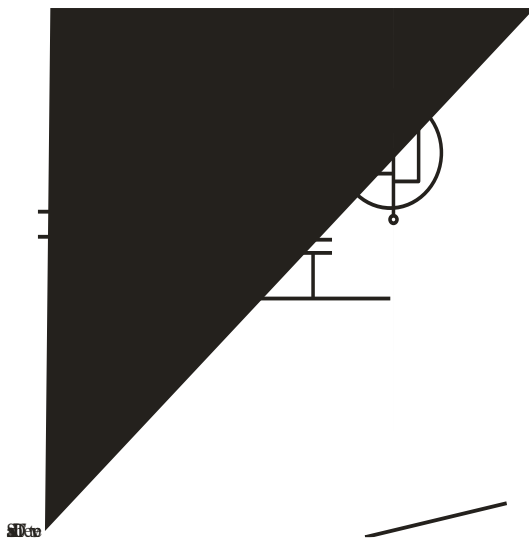
**Avalanche Test Circuit**



**Switching Time Test Circuit**



**Gate Charge Test Circuit**

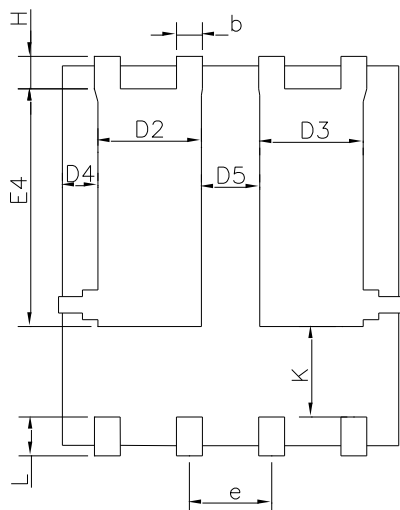
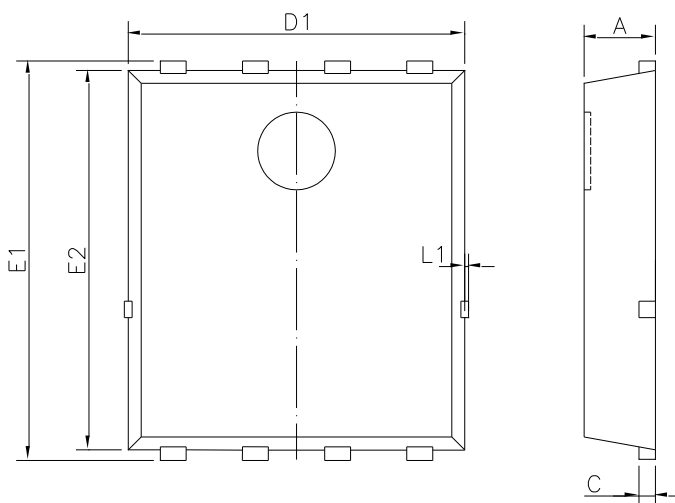


## Device Per Unit

Package Type	Unit	Quantity
PDFN8L(5x6)	Reel	5000

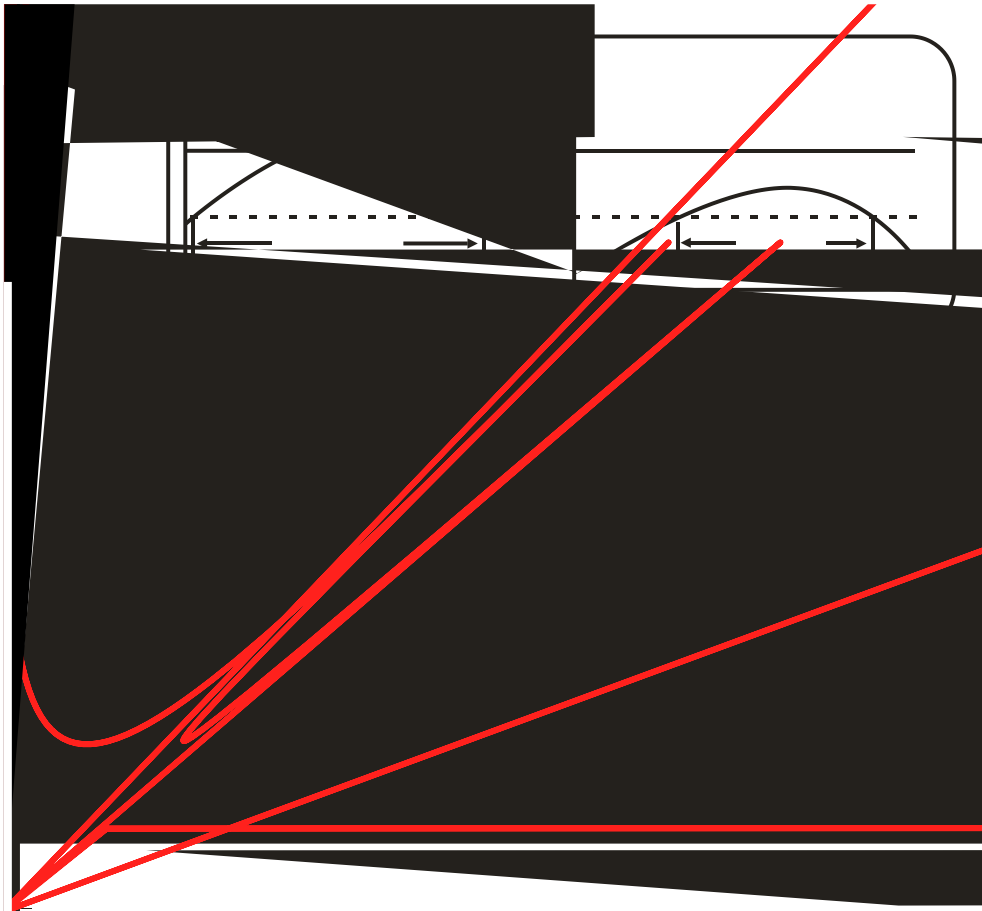
## Package Information

PDFN8L(5x6)



COMMON DIMENSIONS			
SYMBOL	mm		
	MIN	NOM	MAX
A	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.154	0.254	0.354
D1	5.00	5.20	5.40
D2	1.40	1.60	1.80
D3	1.40	1.60	1.80
D4	0.45	0.55	0.65
D5	0.70	0.90	1.10
e	1.27BSC		
E1	5.95	6.15	6.35
E2	5.66	5.86	6.06
E4	3.47	3.67	3.87
H	0.40	0.50	0.60
K	1.23	1.38	1.53
L	0.30	0.60	0.70
L1	\	\	0.12
*Not specified			

**Classification Profile**



**Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{min}$ )	100 °C	150 °C
Temperature max ( $T_{max}$ )	150 °C	200 °C
Time ( $T_{min}$ to $T_{max}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{max}$ to $T_P$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217 °C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature ( $T_P$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_P$ )** within 5°C of the specified classification temperature ( $T_C$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_P$ to $T_{max}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature ( $T_P$ ) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature ( $t_P$ ) is defined as a supplier minimum and a user maximum.		



Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

<b>Package Thickness</b>	<b>Volume mm<sup>3</sup> &lt;350</b>	<b>Volume mm<sup>3</sup> ≥350</b>
<2.5 mm	235 °C	220 °C
2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

<b>Package Thickness</b>	<b>Volume mm<sup>3</sup> &lt;350</b>	<b>Volume mm<sup>3</sup> 350-2000</b>	<b>Volume mm<sup>3</sup> ≥2000</b>
<1.6 mm	260 °C	260 °C	260 °C