

P-Channel Enhancement Mode MOSFET

Feature

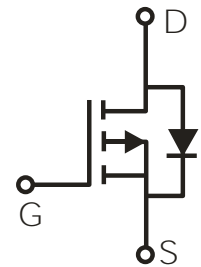
- -30V/-11A
 $R_{DS(ON)} = 11.6\text{ m}\Omega(\text{typ.}) @V_{GS} = -10\text{V}$
 $R_{DS(ON)} = 20\text{ m}\Omega(\text{typ.}) @V_{GS} = -4.5\text{V}$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available
 (RoHS Compliant)

Pin Description



Applications

- Switching application
- Li-battery protection
- DC-DC
- Motor control



Single P-Channel MOSFET

Ordering and Marking Information

HYG 130P03LQ XYMXXXXX	Package Code S:SOP8L Date Code XYMXXXXX
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Note: HUAYI halogen free products contain molding compounds and 100% matte tin plate Termination finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free requirements of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines “Green” to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.

Absolute Maximum Ratings T

Symbol	Parameter	Rating	Unit
Common to all packages unless otherwise specified. T _{amb} = 25°C Unless Otherwise Specified			
V _{DSS}		-30	V
V _{GSS}		±20	V

J T_J Junction Temperature

Electrical Characteristics (Cont.) (T_c =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG130P03LQ1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	4.9	-	
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-25V, Frequency=1MHz	-	1378	-	pF
C _{oss}	Output Capacitance					
C _{rss}	Reverse Transfer Capacitance					
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-15V, R _G =2.5Ω, I _{DS} =-15A, V _{GS} =-10V	-	8.7	-	ns
T _r	Turn-on Rise Time					
t _{d(OFF)}	Turn-off Delay Time					
T _f	Turn-off Fall Time					
Gate Charge Characteristics						
Q _g	Total Gate Charge(V _{GS} =-10V)	V _{DS} =-24V, I _{DS} =-15A	-	32.9	-	nC
Q _g	Total Gate Charge(V _{GS} =-4.5V)					
Q _{gs}	Gate-Source Charge					
Q _{gd}	Gate-Drain Charge					
V _{plateau}	Gate plateau voltage		-	-3.8	-	V

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

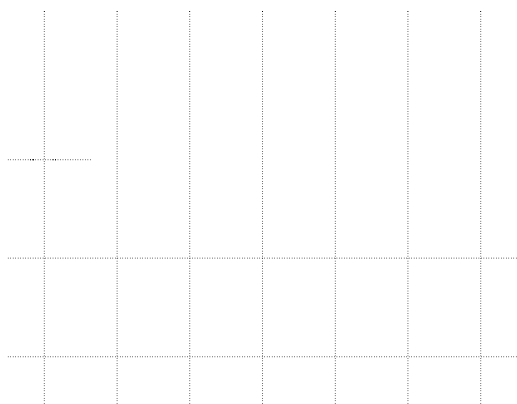


Figure 2: Drain Current

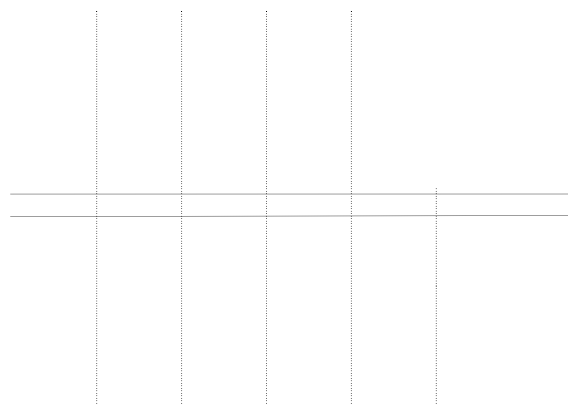


Figure 3: Safe Operation Area

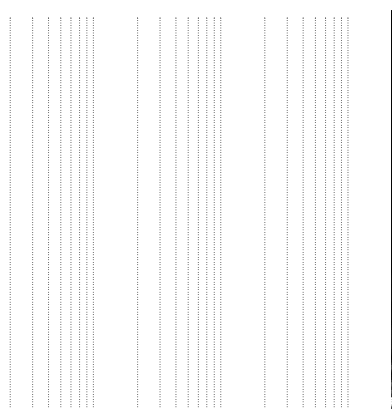


Figure 4: Thermal Transient Impedance

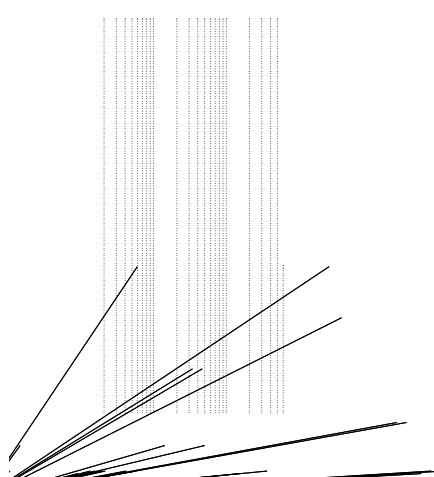


Figure 5: Output Characteristics

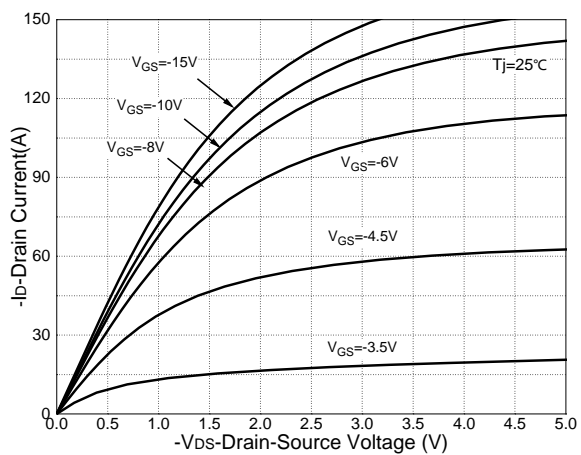


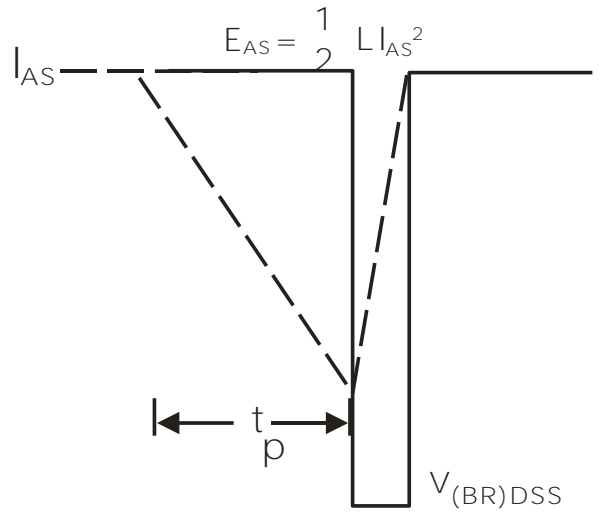
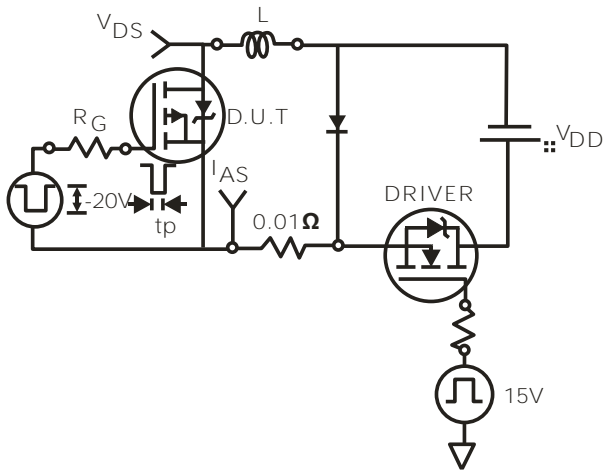
Figure 6: Drain-Source On Resistance



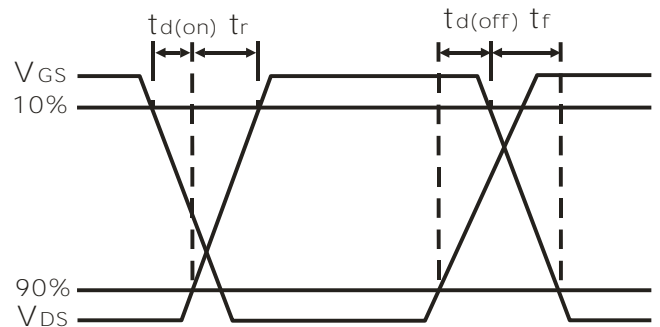
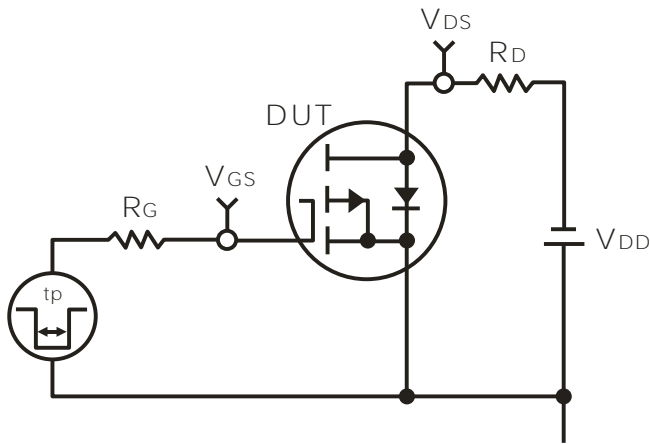
Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

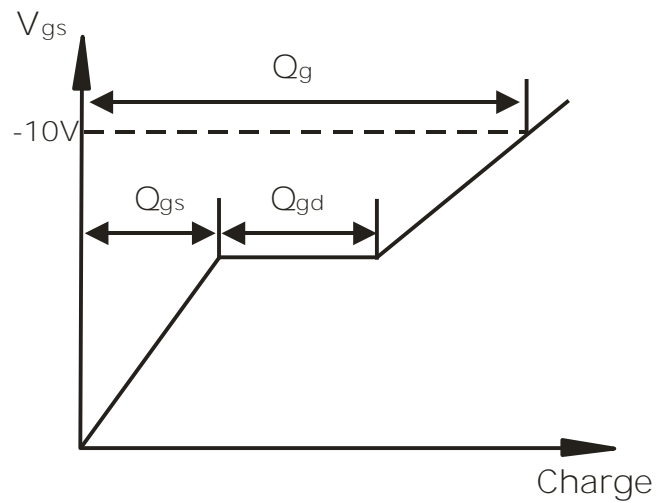
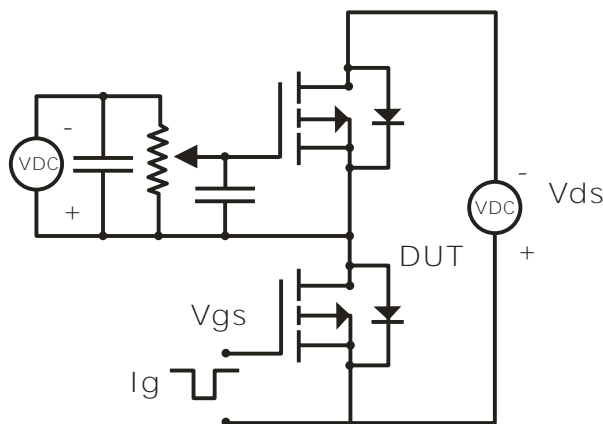
Avalanche Test Circuit



Switching Time Test Circuit



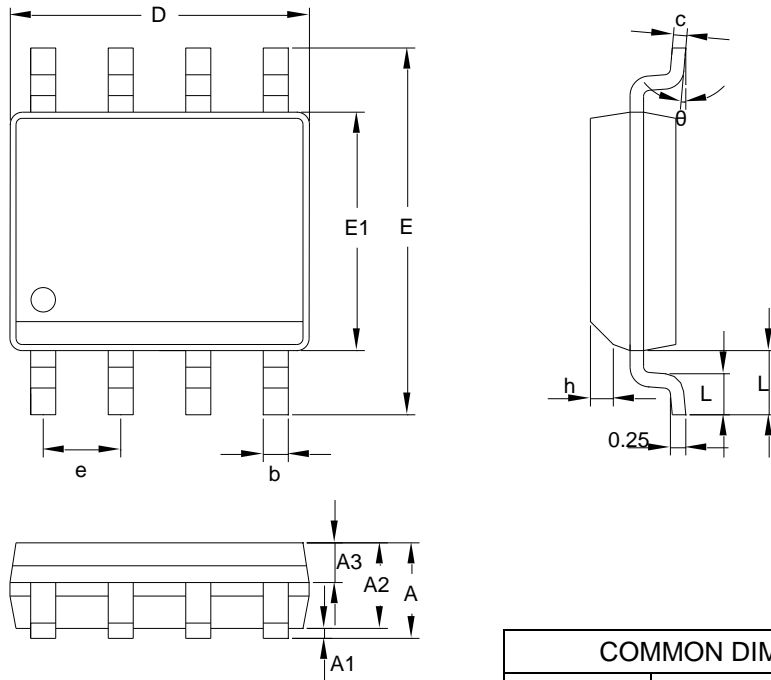
Gate Charge Test Circuit



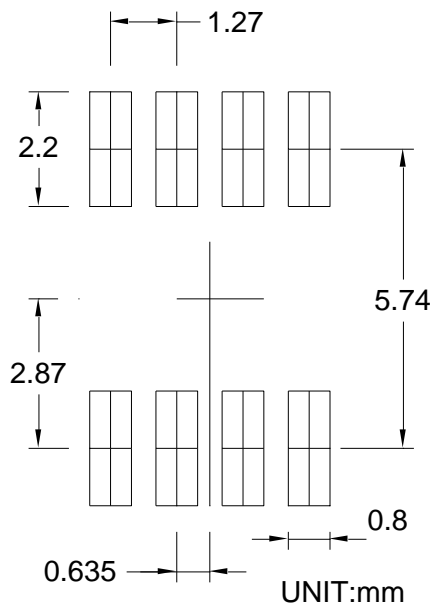
Device Per Unit

Package Type	Unit	Quantity
SOP8L	Reel	2500

Package Information



RECOMMENDED LAND PATTERN

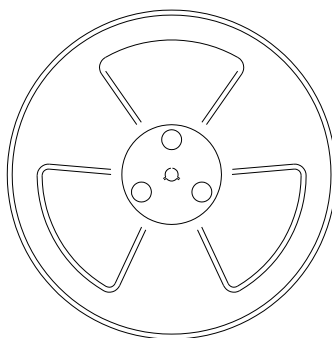
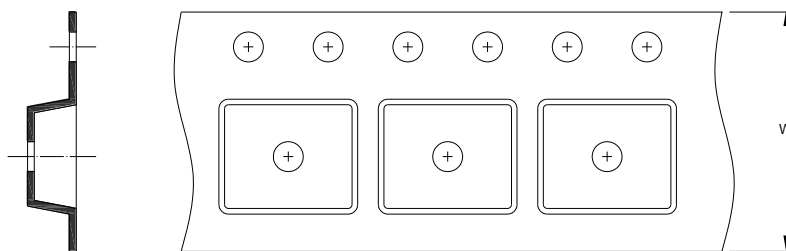


COMMON DIMENSIONS			
SYMBOL	mm		
	MIN	NOM	MAX
A	-	-	1.75
A1	0.10	-	0.225
A2	1.30	1.40	1.50
A3	0.60	0.65	0.70
b	0.39	-	0.47
c	0.20	-	0.24
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
h	0.25	-	0.50
L	0.50	-	0.80
L1	1.05 REF		
θ	0°	-	8°

Note:

1. Follow JEDEC MS-012AA.
2. Dimension D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
3. Dimension E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 10 mil per side.

Carrier Tape & Reel Dimensions

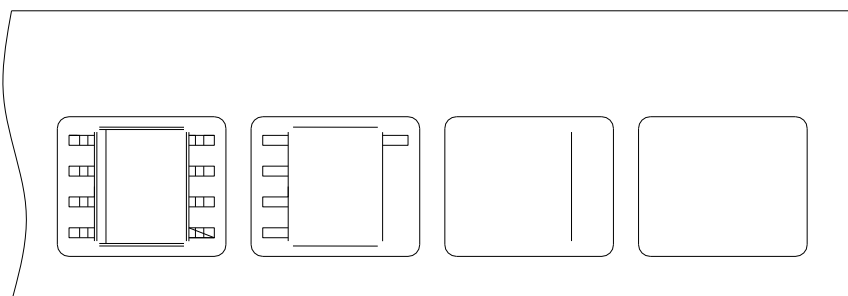


d

Application	A	H	T1	C	d	D	W	E1	F
SOP8L	330 2.00	50 MIN	12.4+2.00 -0.20	13.0+0.50 -0.20	1.5 MIN	20.2 MIN	12.0 0.30	1.75 0.10	5.5 0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 0.10	8.0 0.10	2.0 0.05	1.5+0.10 -0.00	1.5 MIN	0.6+0.00 -0.40	6.40 0.20	5.20 0.20	2.10 0.20

Taping Direction Information

USER DIRECTION OF FEED



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_P)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_P to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_P) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, V _{gs} 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

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